

ocket No.: 061282-0068

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277  
Akinori HAZA, et al. : Confirmation Number: 7166  
Application No.: 10/809,796 : Group Art Unit: 2811  
Filed: March 26, 2004 : Examiner: Nitin Parekh  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

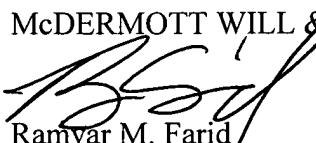
This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

**10/809,796**

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP



Ramyar M. Farid  
Registration No. 46,692

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 RMF:MaM  
Facsimile: 202.756.8087  
**Date: February 17, 2006**

**Please recognize our Customer No. 20277  
as our correspondence address.**



FEB 17 2006

SHEET 1 OF 1

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Include copy of this form with next communication to applicant.